

70014  
SEP 2 1997  
PATENT OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1 Application Serial No. .... 08/530,661  
2 Filing Date ..... September 20, 1995  
3 Inventor ..... Brent Keeth  
4 Assignee ..... Micron Technology, Inc.  
5 Group Art Unit ..... 2503  
6 Examiner ..... N. Kelly  
7 Attorney's Docket No. .... MI22-356  
8 Title: Semiconductor Memory Circuit

SUPPLEMENTAL RESPONSE TO MARCH 19, 1997 OFFICE ACTION

9 To: BOX NON-FEE AMENDMENT  
10 Assistant Commissioner For Patents  
11 Washington, D.C. 20231  
12  
13 From: David G. Latwesen (Tel. 509-624-4276; Fax 509-838-3424)  
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AMENDMENTS

In The Specification.

- 17 At p. 2, ln. 19, after "pitch", insert --of--.  
18  
19 At p. 9, ln. 10, replace "ares" with --areas--.  
20  
21 At p. 27, ln. 20, replace "result sin" with --results in--.  
22  
23  
24